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| **CMPE 140 (Section 01)**  **HW#0** |
| **Due: Tuesday, Sep. 3, 11:59 pm**  **Total Score: /0** |
| **Instructor: Hyeran Jeon** |
| **Computer Engineering Department, San Jose State University** |

In this homework, you will introduce yourself and your background knowledge about computer architecture.

1. Your name and student ID

Name: Sidarth Shahri

Student ID: 009712248

1. Your headshot photo (that shows your face clearly)



1. Explain your knowledge on computer architecture, FPGA design, and MIPS programming briefly (e.g., have you taken related courses?)

I took CMPE 125 at SJSU during the Spring 2018 semester. The course covered Digital Design II material. Despite earning an A in the class, I struggled with learning Verilog and heavily depended on my teammate in lab to help me along. Thus, my knowledge of Verilog and FPGA design is very limited.

I also took CMPE 102 during the Spring 2017 semester. The course covered Assembly Language material, but not in MIPS. We learned PIC24. Therefore, my knowledge of MIPS is little to none.

I have some knowledge of computer architecture through CMPE courses taken at SJSU, but not enough to be considered intermediate or above.